Dongwoon Anatech Licenses Codasip's Bk3 RISC-V Processor for Motor Control ICs for Mobile Camera

Brno, Czech Republic, and Seoul, Korea – March 23rd, 2018 – Codasip, the leading supplier of RISC-V® embedded processor IP, announced today that Dongwoon Anatech, a technology leader in analog and power ICs for mobile phones, has selected Codasip's Bk3 processor and Studio design tool for its next generation family of motor control IC products.

Dongwoon Anatech, fabless analog semiconductor specialist, offers a wide range of analog products, including auto-focus driver IC for smartphones, AMOLED DC-DC converter, display power driver IC, and haptic driver IC.

"We are pleased to announce our selection of Codasip's Bk3 processor for our next motor control IC products. Analysis of the computational needs of our application showed that the RISC-V instruction set with custom DSP extensions was able to deliver the performance we require while keeping silicon area to a minimum," stated Jin Park, CTO, Dongwoon Anatech. *"The best-in-class Studio development tools enable us to profile our software and find an optimal set of instructions for our application".*

The Codasip Bk3 processor, based on the RISC-V open instruction set architecture (ISA) definition, features a single 3-stage in-order execution processor pipeline, and offers optional caches, IEEE 1149.1 debug, and industry standard bus interfaces. Further, the Bk3 – like all Codasip RISC-V implementations – is fully configurable and extensible, offering great advantage over traditional, fixed-configuration processor IP cores.

"We are delighted that Dongwoon Anatech has chosen Codasip to be its provider of RISC-V processor IP and associated design tools," stated Chris Jones, Codasip's Vice President of Marketing. *"Dongwoon Anatech is a major player in providing analog integrated circuits for mobile phones, and thus needs efficient digital subsystems. Codasip technology provides them with a robust processor solution complete with a comprehensive high-performance software toolchain."*

About Dongwoon Anatech

Dongwoon Anatech is a 'World class 300' company that was established in 2006 and has grown over the past three decades to stand shoulder to shoulder with global fabless companies. Dongwoon Anatech has earned the world's largest market share in the AF driver IC for smartphones, and has many customers including global clients such as Samsung, LG, Sony, and Huawei. In addition, Dongwoon Anatech plans to lead the world market by developing a variety of products including the haptic driver IC, DC-DC converter, display driver IC, and LED driver IC.

For more information, visit www.dwanatech.com.

About Codasip

Codasip delivers leading-edge processor IP and high-level design tools that provide ASIC designers with all the advantages of the RISC-V open-standard ISA, along with the unique ability to automatically optimize the processor IP. As a founding member of the RISC-V Foundation and a long-term supplier of LLVM and GNU-based processor solutions, Codasip is committed to open standards for embedded processors.

Formed in 2006 and headquartered in Brno, Czech Republic, Codasip currently has offices in the US and Europe, with representatives in Asia and Israel.

For more information about Codasip's products and services, visit www.codasip.com.

About RISC-V

RISC-V is an open, free instruction set architecture (ISA) enabling a new era of processor innovation through open standard collaboration. Born in academia and research, RISC-V ISA delivers a new level of free, extensible software and hardware freedom on architecture, paving the way for the next 50 years of computing design and innovation.

For more information, visit www.riscv.org.