

Codalip Welcomes Jerry Ardizzone to Executive Team as Vice President of Worldwide Sales

Campbell, California – April 9th, 2018 – Codalip, the leading supplier of RISC-V[®] embedded processor intellectual property, today announced that Jerry Ardizzone has joined as Vice President of Worldwide Sales, reporting to CEO Karel Masařík.

“We are very excited to have Jerry on board to lead our global sales efforts,” declared Karel Masařík, CEO and founder of Codalip. “As RISC-V momentum grows, Codalip’s reach is expanding worldwide, and we need the leadership and international expertise that Jerry has demonstrated throughout his career in the IP industry. Jerry’s hire represents the completion of building an experienced and accomplished executive team, capable of executing our strategy of delivering the broadest portfolio of RISC-V processors and developer tools in the industry.”

Ardizzone has over 30 years of relevant industry experience with a strong track record of delivering business growth, including roles as Executive Vice President of Sales at ARM and Senior Vice President of Sales at Tensilica. Most recently, he was Vice President of Worldwide Sales at PLDA, a leading provider of PCI-E controller IP. He officially assumed his role at Codalip on April 2nd.

“During my career, I have been privileged to work for several companies that have brought industry-transforming processor technology to the market, beginning with the 68000. I am convinced that Codalip is on a similar trajectory, as the semiconductor community is ready for a new standard to emerge,” said Ardizzone. “Codalip with its configurable RISC-V portfolio is a young company of tremendous potential, and is on its way to building a true global presence.”

About Codalip

Codalip delivers leading-edge processor IP and high-level design tools that provide ASIC designers with all the advantages of an open standard, such as the RISC-V ISA, along with the unique ability to automatically optimize the processor IP. As a founding member of the RISC-V foundation and a long-term supplier of LLVM and GNU-based processor solutions, Codalip is committed to open standards for embedded processors. Formed in 2006 and headquartered in Brno, Czech Republic, Codalip currently has offices in the US and Europe, with representatives in Asia and Israel. More information on Codalip’s products and services is available at www.codasip.com.

About RISC-V

RISC-V is an open, free instruction set architecture (ISA) enabling a new era of processor innovation through open standard collaboration. Born in academia and research, RISC-V ISA delivers a new level of free, extensible software and hardware freedom on architecture, paving the way for the next 50 years of computing design and innovation. For more information, visit www.riscv.org.