

Cudasip Enters Partner Agreement with Japan Marketing Office

Campbell, California – September 10th, 2018 – Cudasip, the leading supplier of RISC-V[®] embedded processor IP, today announced that it has signed a representative agreement with Japan Marketing Office (JMO) to boost Cudasip’s presence in the Japan semiconductor and systems technology markets.

JMO was established in 2011 to provide sales, distribution and marketing services to create significant, sustainable and profitable sales and growth to global technology companies that want to enter the Japanese market.

“The RISC-V movement represents an opportunity for Japanese semiconductor companies to break free of legacy processor architectures and create innovative new products. Cudasip’s processors and tools are ideal for such companies that want to differentiate,” stated Nobuyuki Imamura, co-founder of JMO. *“We are excited to have Cudasip on board, and we look forward to helping them grow their business here.”*

“Japanese companies were among the very first to recognize the advantages of Cudasip technology and embrace it as a key part of their design methodology,” stated Jerry Ardizzone, Vice President of Worldwide Sales at Cudasip. *“With the help of the experienced team of professionals at JMO, we will undoubtedly grow our presence in Japan.”*

About RISC-V

RISC-V (pronounced “risk-five”) is an open, free ISA enabling a new era of processor innovation through open standard collaboration. Born in academia and research, RISC-V ISA delivers a new level of free, extensible software and hardware freedom on architecture, paving the way for the next 50 years of computing design and innovation.

For more information about RISC-V, visit www.riscv.org.

About Cudasip

Cudasip delivers leading-edge processor IP and high-level design tools, providing ASIC designers with all the advantages of the RISC-V open ISA, along with the unique ability to automatically optimize the processor IP. As a founding member of the RISC-V Foundation and a long-term supplier of LLVM and GNU-based processor solutions, Cudasip is committed to open standards for embedded processors. Formed in 2006 and headquartered in Brno, Czech Republic, Cudasip currently has offices in the US and Europe, with representatives in Asia and Israel.

For more information about Cudasip’s products and services, visit www.cudasip.com.

About JMO

For more information about JMO, contact them at info@japanmarketingoffice.com.