

CODASIP ANNOUNCES A NEW DESIGN CENTER IN FRANCE

Munich, Germany – October 20th, 2020 – Codalip, the leading supplier of customizable RISC-V embedded processor IP, announced its new strategic design center today. It is based in France and the local Codalip team is going to play an important role in the company's innovation and development plans for the next-generation product IP.

The new Codalip French Design Center started operation in the summer. It is located in Villeneuve Loubet on the Mediterranean coastline in southeast France, near the technology park of Sophia Antipolis where major technology companies such as Bosch, NXP, Thales, and many others have offices.

*“As we are expanding the product portfolio and fostering our technology leadership, we need talented senior engineers to support the growth,” explained **Karel Masařík, CEO of Codalip**. “Sophia Antipolis is an important European location for R&D and technical talent in the semiconductor industry. We are proud to have hired our French colleagues there, close to customers. Their expertise will be a great supplement to that of our main R&D Center in the Czech Republic.”*

The newly established Codalip French team is currently focused mainly on product architecture and verification. The team's leader is **Méline Facon**, an industry professional with rich experience in the semiconductor IP design field.

*“The RISC-V open ISA is revolutionizing the deployment of processors in SoCs by being a neutral standard independent of any single company,” said **Méline Facon, Director of the French Design Center**. “We are confident that our team's experience can help respond to the rapidly increasing demand for RISC-V processors, and we are really looking forward to becoming a part of Codalip and its RISC-V story.”*

The full address of the Codalip French Design Center is available on the Codalip website, section [Contact](#).

About Cudasip

Cudasip delivers leading-edge processor IP and high-level processor design tools, providing ASIC designers with all the advantages of the RISC-V open ISA, along with the unique ability to automatically optimize the processor IP. As founding member of the RISC-V Foundation and a long-time supplier of LLVM and GNU-based processor solutions, Cudasip is committed to open standards for embedded processors. Formed in 2014 and headquartered in Munich, Germany, Cudasip currently has offices in Europe and China, with sales representatives worldwide. For more information about our products and services, please visit www.codasip.com.

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