

CODASIP RISC-V PROCESSORS FPGA EVALUATION PLATFORMS

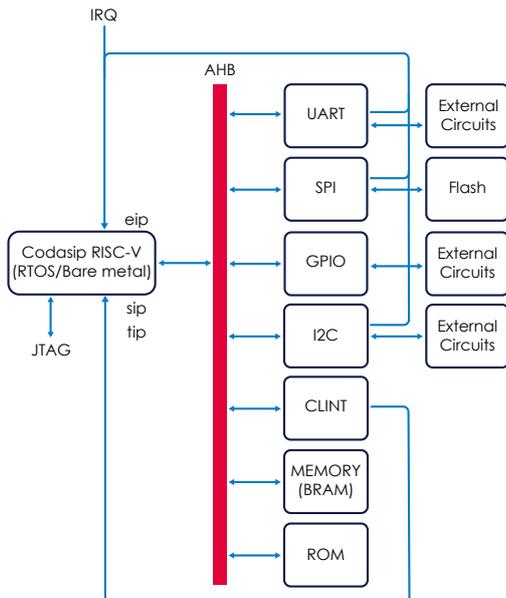
Easily evaluate any of the Codalisp RISC-V Processors using two new FPGA Evaluation Platforms. Targeting popular Digilent boards, the platforms are supplied with everything you need to get started in a matter of minutes.

The FPGA Evaluation Platforms have been created to greatly simplify the Codalisp IP evaluation process. There are two types of the platform, for **Embedded** and for **Application** cores. With either platform, you'll get started and have your first C program running in 15 minutes.

The Codalisp Evaluation Platforms consist of the selected RISC-V processor IP core with a subsystem containing peripherals and AMBA interconnect. A testbench layer includes a clock generator and block RAMs for internal memories, and can use some of the FPGA peripherals such as flash memory. Additionally, you will receive a Vivado project and bitstream files for their target FPGA board. The platforms include an SDK, the Codalisp CodeSpace IDE, software examples, and step-by-step documentation on how to use the peripherals.

PLATFORM FOR EMBEDDED CORES

The Codalisp Embedded Processor Evaluation Platform based on AHB is suitable for low power and high performance embedded cores such as the Codalisp **L11**, **31**, **L50**, and **H50**. It can be used for evaluating systems with either bare metal software or RTOS (e.g. FreeRTOS).

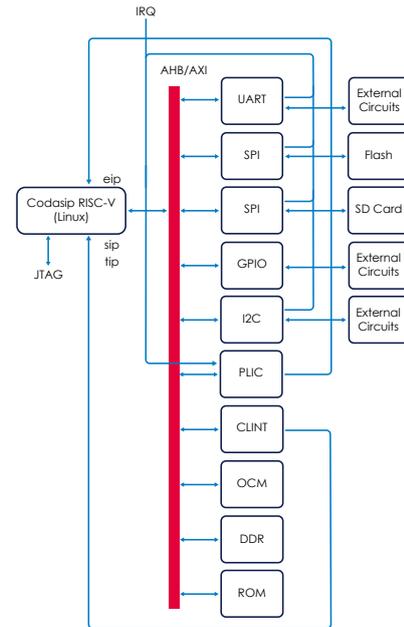


Codalisp Evaluation Platform for Embedded Cores.

The bitstream files target the **Digilent Nexys A7**, a cost-effective, yet powerful and highly customizable FPGA development board with a rich selection of interfaces.

PLATFORM FOR APPLICATION CORES

The Codalisp Application Processor Evaluation Platform, using either AHB or AXI, aims to support single- or multi-core systems based on application processors such as the **Codalisp A70** and **A70-MP**. The platform comes with flash images for embedded Linux and documentation to explain compilation of Linux and user applications.



Codalisp Evaluation Platform for Application Cores.

For a simple single-core A70 evaluation, the **Digilent Nexys A7** board can be targeted. This platform is best for evaluating embedded applications such as AI/ML.

For multi-core systems, the more complex Digilent boards are required:

- **Digilent Nexys Video** (MP1 or MP2), a powerful platform for audio/video applications, equipped especially for high-bandwidth and low-latency A/V processing.
- **Digilent Genesys 2** (MP4), a high-performance, ready-to-use FPGA development board, suited for multimedia applications but flexible enough for almost any project thanks to a wide choice of peripherals.

GET THE PACKAGE

To get a quote for a Codalisp RISC-V Processor and obtain your Evaluation Package, you can use the online form at www.codalisp.com/risc-v-processors, or contact us directly at contact@codalisp.com.

Please note that obtaining the package is subject to signing an Evaluation Agreement.